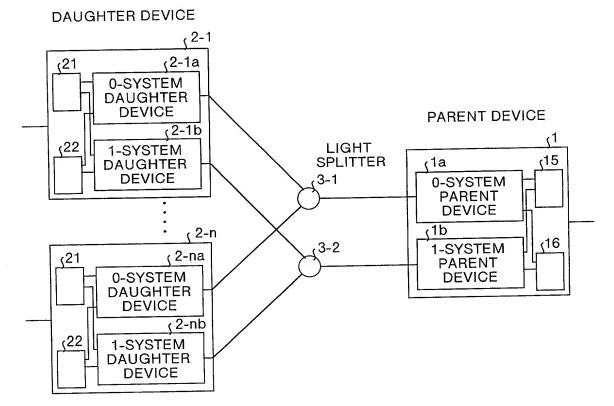
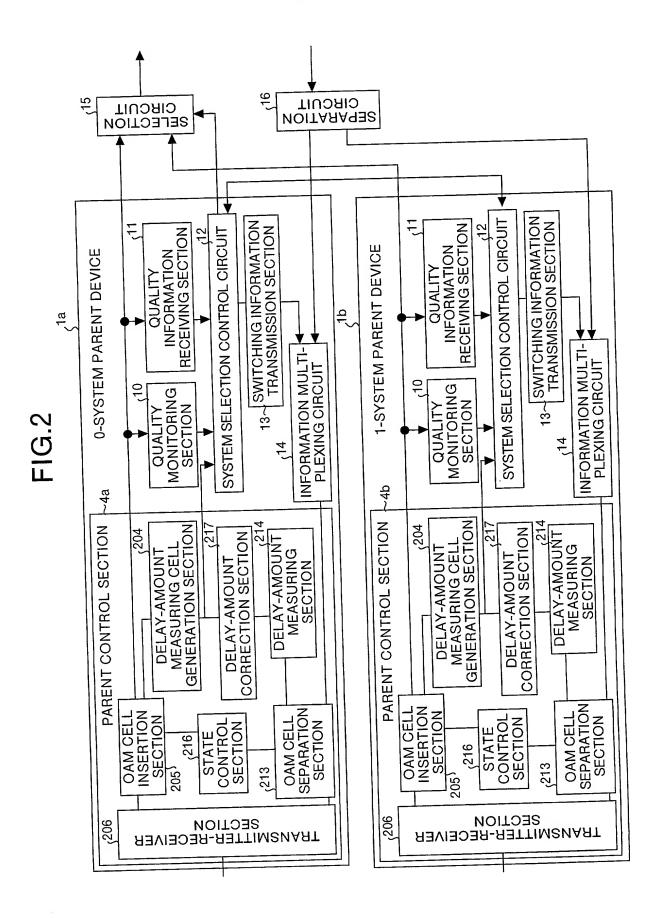
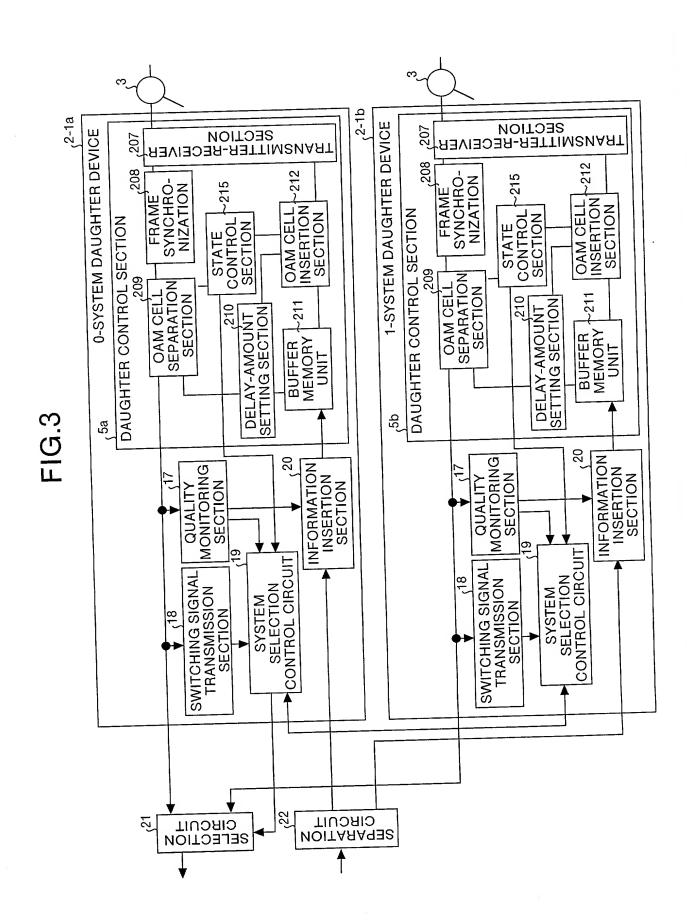
FIG.1



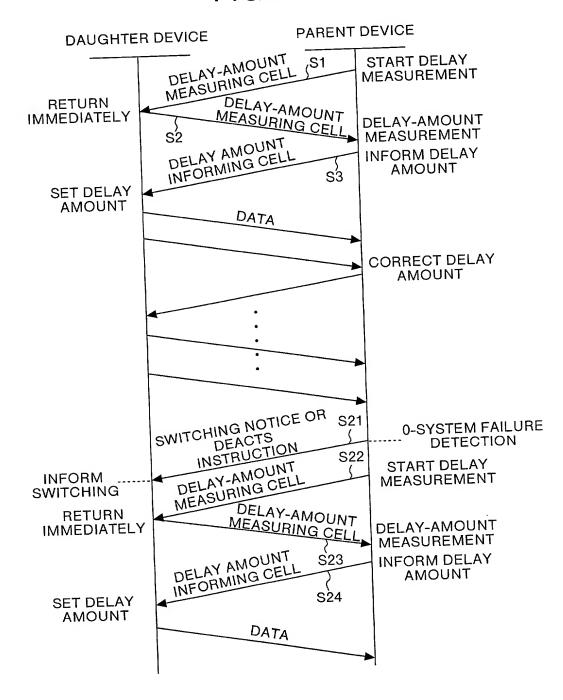






The state of the s

FIG.4



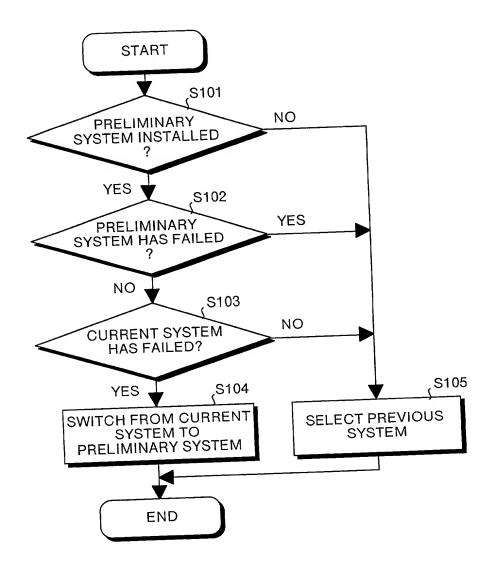
STATE (0-SYSTEM) OR OF STATE OF EACH DAUGHTER DEVICE (1: OPERATIVE 0: FAILED)	STATE (1-SYSTEM) OR OF STATE OF EACH DAUGHTER DEVICE (1: OPERATIVE 0: FAILED)	SELECTED SYSTEM
1	1	PREVIOUS SYSTEM
0	1	1-SYSTEM
1	0	0-SYSTEM
0	0	PREVIOUS SYSTEM

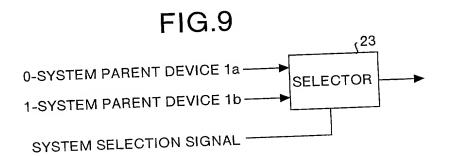
NUMBER	STATE (0-SYSTEM)	STATE (1-SYSTEM)	SELECTED SYSTEM
1	OPERATIVE	OPERATIVE	0-SYSTEM
2	OPERATIVE	OPERATIVE	0-SYSTEM
3	OPERATIVE	OPERATIVE	0-SYSTEM

FIG.7

NUMBER	STATE (0-SYSTEM)	STATE (1-SYSTEM)	SELECTED SYSTEM
1	FAILED	OPERATIVE	1-SYSTEM
2	OPERATIVE	OPERATIVE	1-SYSTEM
3	OPERATIVE	OPERATIVE	1-SYSTEM

FIG.8





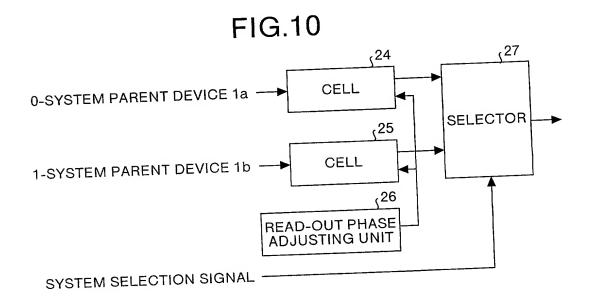


FIG.11

STATE (0-SYSTEM) (1: OPERATIVE 0: FAILED)	STATE (1-SYSTEM) (1: OPERATIVE 0: FAILED)	SELECTED SYSTEM
1	1	PREVIOUS SYSTEM
0	1	1-SYSTEM
1	0	0-SYSTEM
0	0	PREVIOUS SYSTEM

FIG.12

NUMBER	STATE (0-SYSTEM)	STATE (1-SYSTEM)	SELECTED SYSTEM
1	FAILED	OPERATIVE	1-SYSTEM
2	OPERATIVE	OPERATIVE	0-SYSTEM
3	OPERATIVE	OPERATIVE	0-SYSTEM

FIG.13

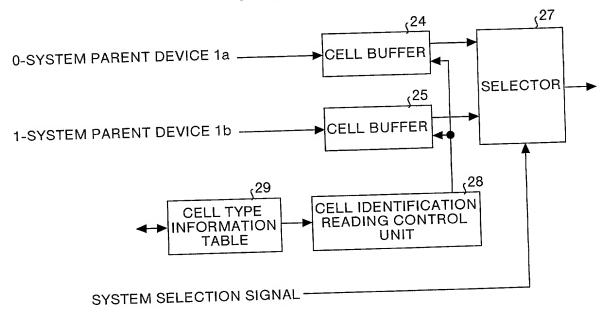


FIG.14

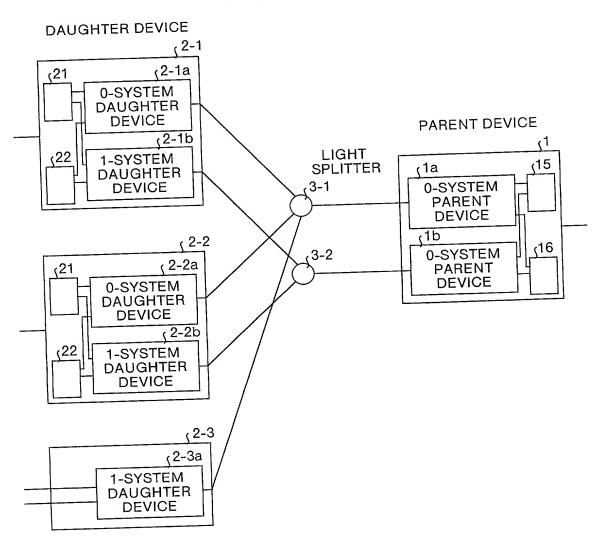


FIG.15

NUMBER	STATE (0-SYSTEM)	STATE (1-SYSTEM)	SELECTED SYSTEM
1	FAILED	OPERATIVE	1-SYSTEM
2	OPERATIVE	OPERATIVE	0-SYSTEM
3	OPERATIVE	IGNORE	FIXED TO 0-SYSTEM

FIG.16

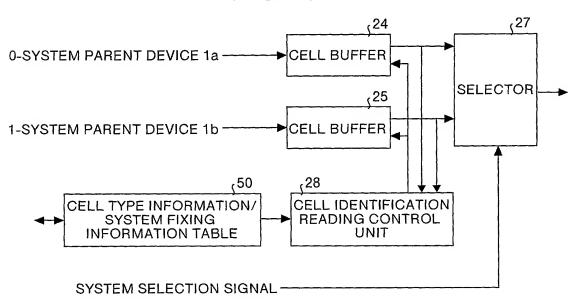


FIG. 17

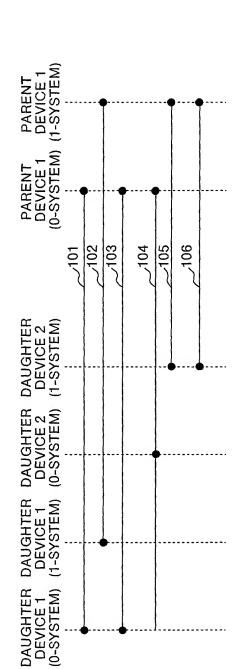
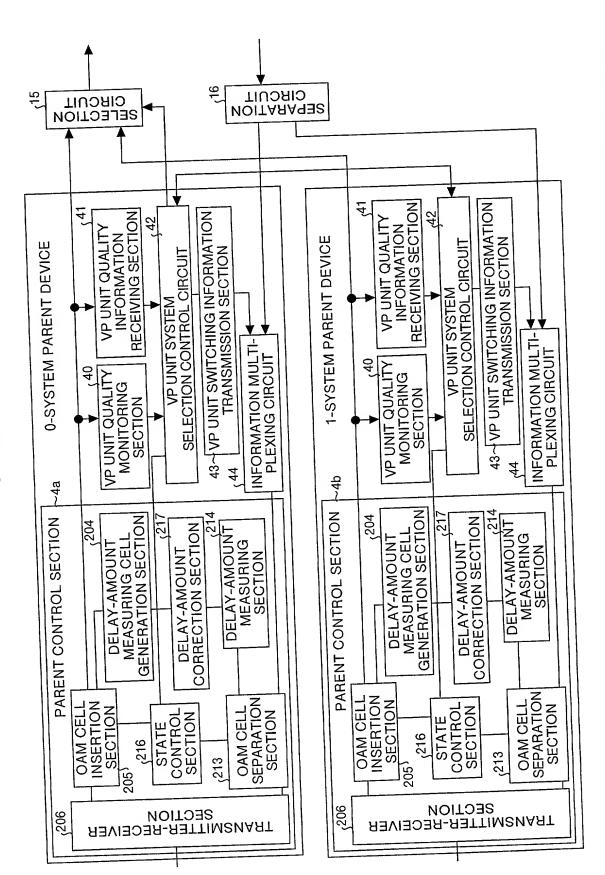


FIG. 18



TRANSMITTER-RECEIVER SECTION

TRANSMITTER-RECEIVER SECTION

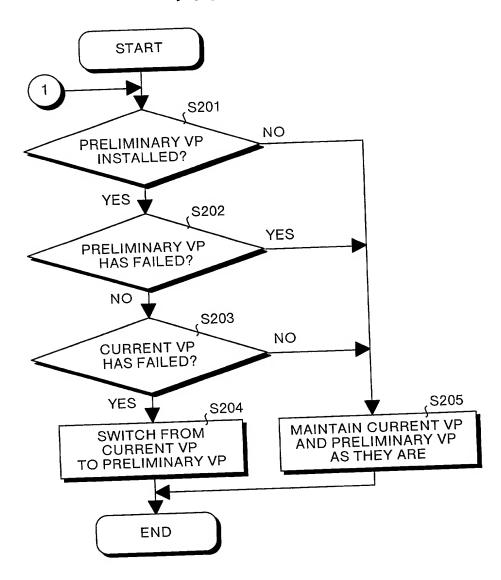
SELECTION CIRCUIT

2-1b

1-SYSTEM DAUGHTER DEVICE 0-SYSTEM DAUGHTER DEVICE OAM CELL ~212 INSERTION SECTION FRAME' SYNCHRO-NIZATION SYNCHRO-NIZATION **√212** ~215 ~ 215 FRAME OAM CELL INSERTION STATE CONTROL SECTION STATE CONTROL SECTION DAUGHTER CONTROL SECTION SECTION DAUGHTER CONTROL SECTION SEPARATION SECTION OAM CELL SEPARATION SECTION OAM CEĽ **←211** ,211 ,210 210 DELAY-AMOUŃT SETTING SECTION SETTING SECTION DELAY-AMOUŃT BUFFER MEMORY BUFFER MEMORY LIND LNN , 5a 2b VP OAM CELL INSERTION SECTION INSERTION SECTION MONITORING SECTION 33 MONITORING SECTION સ્ ဗ္ပ QUALITY QUALITY VP UNIT VP UNIT VP OAM CELL SELECTION CONTROL CIRCUIT CONTROL CIRCUIT VP UNIT SWITCHING SIGNAL RECEIVING VP UNIT SWITCHING **VP UNIT SYSTEM VP UNIT SYSTEM** SIGNAL RECEIVING SELECTION 32 32 SECTION SECTION 33) 33)

SEPARATION CIRCUIT

FIG.20



VPI NUMBER	VP STATE (0- SYSTEM)	VP STATE (1- SYSTEM)	SELECTED SYSTEM	SELECTION METHOD
1	FAILED	OPERATIVE	1-SYSTEM	SELECT NORMAL SYSTEM OR PREVIOUS SYSTEM
2		_	FIXED TO 0-SYSTEM	FIXEDLY SELECT SYSTEM THAT IS PATH SET
3	OPERATIVE	OPERATIVE	0-SYSTEM	SELECT NORMAL SYSTEM OR PREVIOUS SYSTEM
4			FIXED TO 1-SYSTEM	FIXEDLY SELECT SYSTEM THAT IS PATH SET

FIG.22

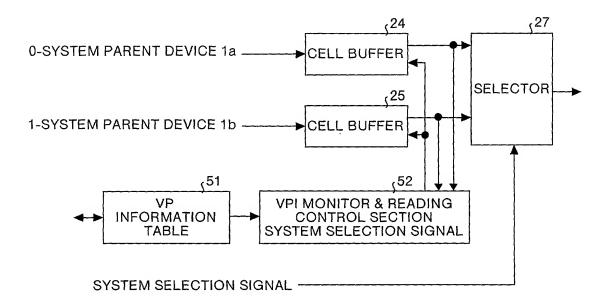
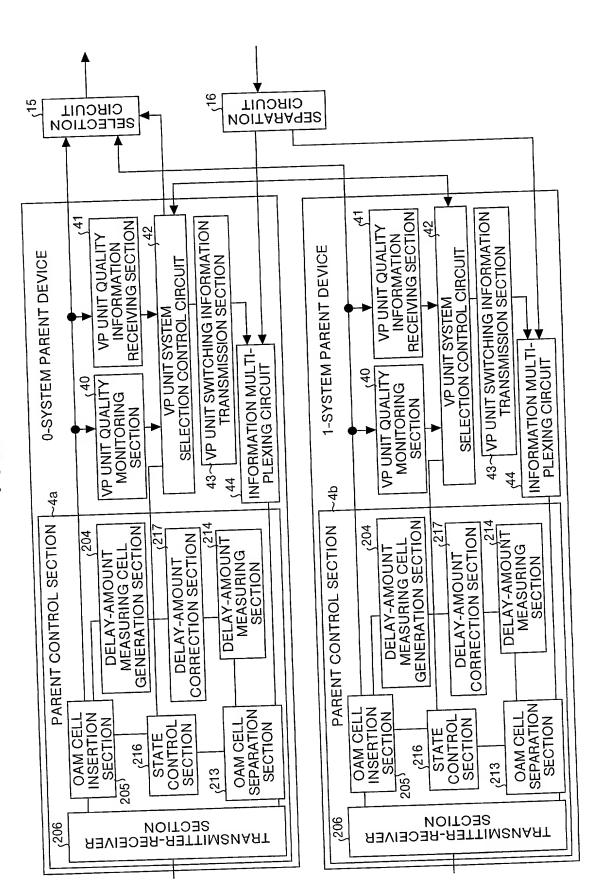
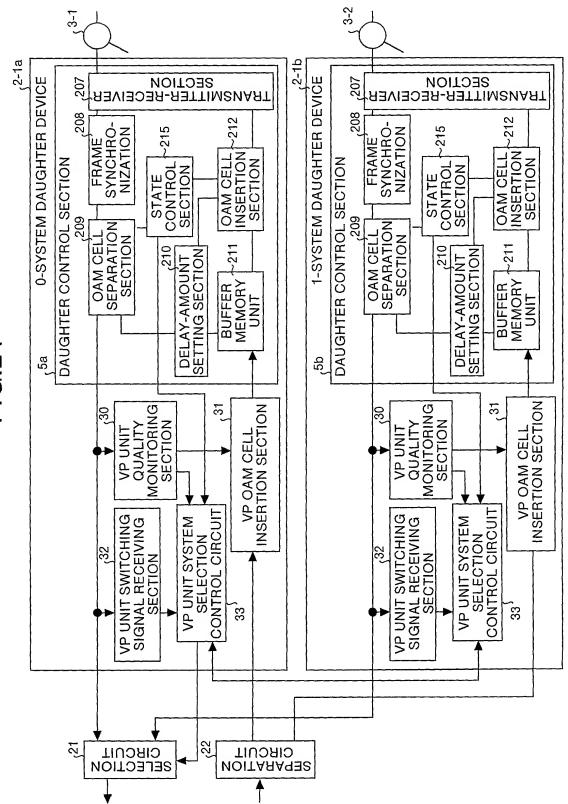


FIG.23



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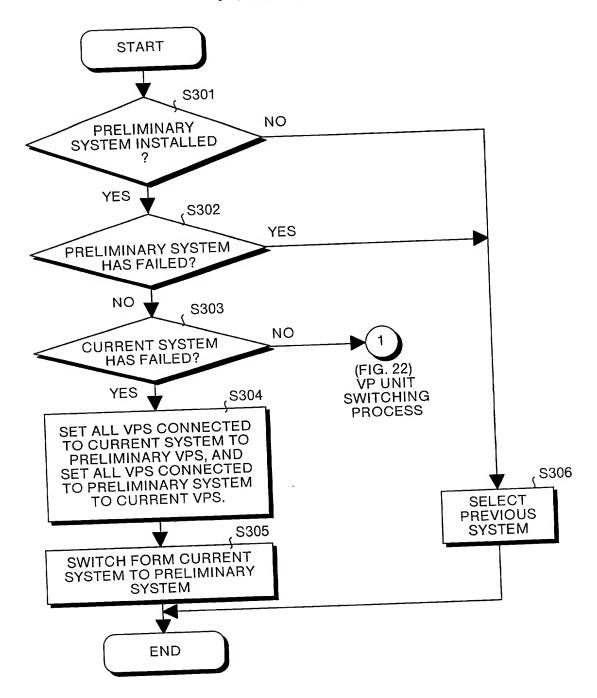


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1

FIG.25



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11.1



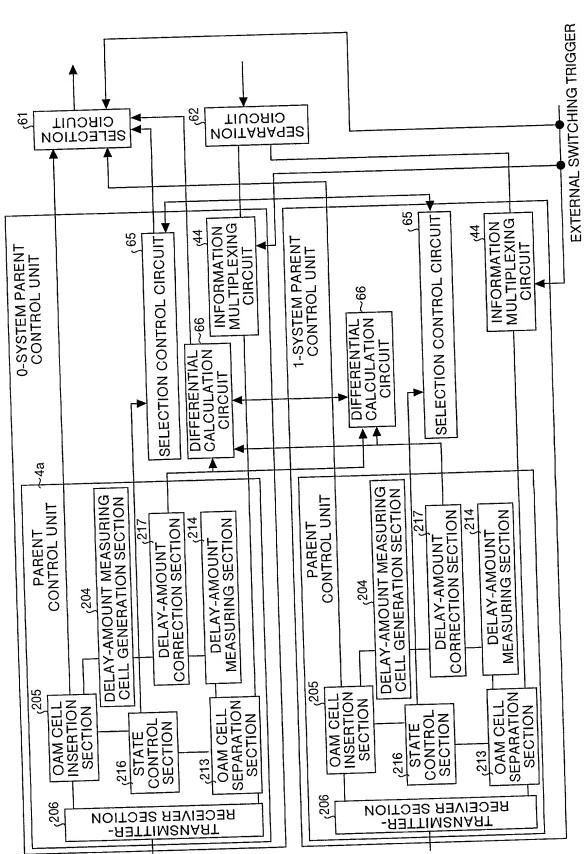
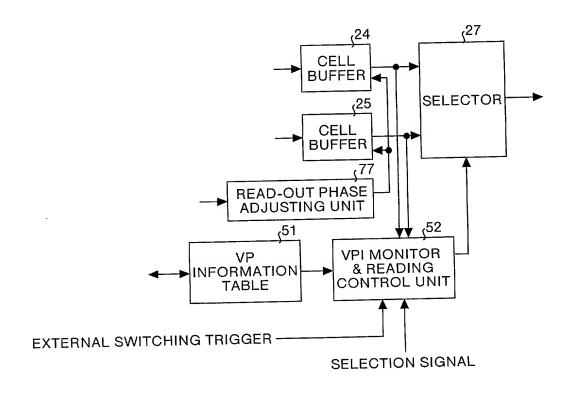


FIG.27



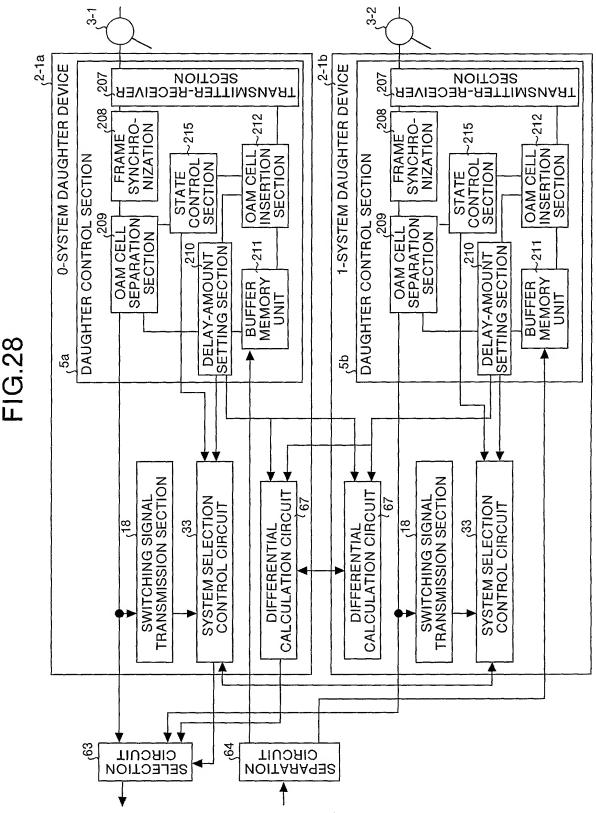


FIG.29

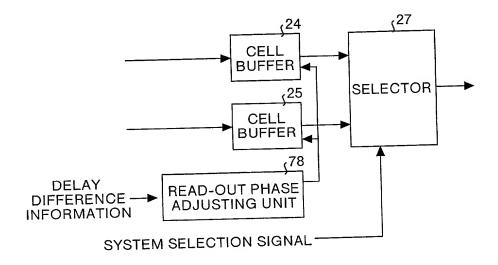


FIG.30

